

alu[ dest, A\_op, alu\_op, B\_op]

(ALU instruction)

Fig. 1A PRIOR ART

A\_Op and B\_Op are symbolic registers, the following are possible register class assignment pairs to A\_Op and B\_Op:

A_Op	B_Op
General Purpose Register (GPR) (A Bank)	GPR (B Bank)
GPR (B Bank)	GPR (A Bank)
Transfer In (SRAM (S) or DRAM (D))	GPR (A Bank or B Bank)
GPR (A Bank or B Bank)	Transfer In (S) or (D)

(Possible register class assignment to A\_Op and B\_Op)

Fig. 1B

PRIOR ART